



gtw

Docket No.: 050432-0655

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
James PAN , et al.	:	Confirmation Number: 7419
Application No.: 10/706,948	:	Group Art Unit: Unknown
Filed: November 14, 2003	:	Examiner: Unassigned

For: LOW-POWER MULTIPLE-CHANNEL FULLY DEPLETED QUANTUM WELL
CMOSFETS

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Each reference was cited in a corresponding foreign application search report or office action and its relevance discussed therein. A copy of the foreign search report or office action is attached for the Examiner's information.

10/706,948

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP



Brian K. Seidleck

Registration No. 51,321

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 BKS:apr
Facsimile: 202.756.8087
Date: March 24, 2005

**Please recognize our Customer No. 20277
as our correspondence address.**

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

 ATTY. DOCKET NO.
050432-0655

 SERIAL NO.
10/706,948

 APPLICANT
James PAN , et al.

 FILING DATE
November 14, 2003

 GROUP
Unknown

(PTO-1449)

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US 5,550,545	3/19/1996	Cheng et al.	
		US 2002/115240	8/22/2002	Nowak et al.	
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number + Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		"New FET on SOI Structure", IBM Technical Disclosure Bulletin, IBM Corp, New York, September 1, 1991, Vol. 34, No. 4A, pg. 369

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.